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Digital Design with CPLD Applications and VHDL / Edition 2 ...

Complex programmable logic devices are ideal for high performance, critical control applications. CPLD can be used in digital designs to perform the functions of boot loader; CPLD is used for loading the configuration data of a field programmable gate array from non-volatile memory. Generally, these are used in small design applications like address decoding

Applications of Complex Programmable Logic Device (CPLD)

This Windows-based software allows users to design, test, and program CPLD designs in text-based (VHDL) and graphic (schematic entry) formats. The Second Edition introduces CPLDs earlier in the teaching sequence, laying a solid foundation for more advanced principles without neglecting underlying digital fundamentals such as Boolean algebra, logic minimization, and combinational and sequential circuits.

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The teaching of digital electronics using CPLD is very useful, the book covers theory, truth table, gate level implementation. The examples show you how to create a module, for instance,a multiplexer.It doesnt only end here, it further demonstrate the application of the multiplexer.

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The CPLD is first to become functional and then configures or initializes the power-up sequence of the other devices on the board. Real-Time ISP: Reduces system downtime because you can download a new program into the MAX II on-board flash while its still operational.

System Configuration and Initialization

The CAD software provides automatic mapping of a design written in Verilog into Field Programmable Gate Arrays (FPGAs) and Complex Programmable Logic Devices (CPLDs). Students will be able to try, firsthand, the book's Verilog examples (over 140) and homework problems.

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